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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Risucci, Christopher R.

Appl. No.: 09/925,314

Filed: August 10, 2001

For: **System and Method of Controlling  
Software Decompression Through  
Exceptions**

Confirmation No.: 4782

Art Unit: 2183

Examiner: Tsai, Henry

Atty. Docket: 1778.0180000

**Supplemental Information Disclosure Statement**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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SEP 09 2004

Technology Center 2100

Sir:

Listed on accompanying Forms PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98. This Supplemental Information Disclosure Statement is supplemental to two electronic Information Disclosure Statements filed the same day hereas. Any fee necessary for consideration of this Supplemental Information Disclosure Statement has been authorized or otherwise submitted with one of the electronic Information Disclosure Statements.

Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicant reserves the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may

not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith.

Applicant has checked the appropriate boxes below.

- ☐ 1. Statement under 37 C.F.R. 1.704(d). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- ☐ 2. Filing under 37 C.F.R. § 1.97(b). This Information Disclosure Statement is being filed within three months of the date of filing of a national application other than a continued prosecution application (CPA), OR within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, OR before the mailing date of a first Office Action on the merits OR before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No statement or fee is required.
- ☒ 3. Filing under 37 C.F.R. § 1.97(c). This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final

Rejection, or Notice of Allowance, or an action that otherwise closes prosecution in the application.

- ☐ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
- ☐ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ☒ c. Payment of the fee under 37 C.F.R. § 1.17(p) in the amount of \$180.00 has been authorized or otherwise submitted with an electronic Information Disclosure Statement filed the same day.
- ☐ 4. Filing under 37 C.F.R. § 1.97(d) This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. Enclosed find our PTO-2038 Credit Card Payment Form in the amount of \$ \_\_\_\_\_ in payment of the fee under 37 C.F.R. § 1.17(p); in addition:

- ☐ a. Statement under 37 C.F.R. § 1.97(e)(1). I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
- ☐ b. Statement under 37 C.F.R. § 1.97(e)(2). I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
- ☐ 5. The document(s) was/were cited in a search report by a foreign patent office in a counterpart foreign application. Submission of an English language version of the search report that indicates the degree of relevance found by the foreign office is provided in satisfaction of the requirement for a concise explanation of relevance. 1138 OG 37, 38.
- ☐ 6. A concise explanation of the relevance of the non-English language document(s) appears below:
- ☒ 7. Copies of the documents are submitted herewith.
- ☐ 8. Copies of the documents were cited by or submitted to the Office in an IDS that complies with 37 C.F.R. § 1.98(a)-(c) in Application No. \_\_\_\_\_, filed

\_\_\_\_\_, which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

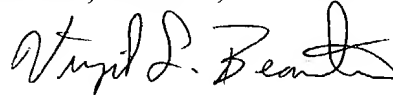
- ☐ 9. No copies of U.S. patents and patent application publications cited on the attached Form PTO-1449 are submitted in accordance with 1276 OG 55 because this application was filed after June 30, 2003.
- ☐ 10. It is expected that the examiner will review the prosecution and cited art in the parent application no(s). \_\_\_\_\_ in accordance with MPEP 2001.06(b), and indicate in the next communication from the office that the art cited in the earlier prosecution history has been reviewed in connection with the present application.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Virgil L. Beaton  
Attorney for Applicant  
Registration No. 47,415

Date: 9/7/04

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FORM PTO-1449

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ATTY. DOCKET NO.

1778.0180000

APPLICATION NO :

09/925,314

FIRST NAMED INVENTOR

Risucci, Christopher R.

FILING DATE

August 10, 2001

ART UNIT

2183

## INFORMATION DISCLOSURE STATEMENT

## U.S. PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB-CLASS | FILING DATE |
|------------------|-----------------|------|------|-------|-----------|-------------|
|                  | AA1             |      |      |       |           |             |
|                  | AB1             |      |      |       |           |             |
|                  | AC1             |      |      |       |           |             |
|                  | AD1             |      |      |       |           |             |
|                  | AE1             |      |      |       |           |             |
|                  | AF1             |      |      |       |           |             |
|                  | AG1             |      |      |       |           |             |
|                  | AH1             |      |      |       |           |             |
|                  | AI1             |      |      |       |           |             |
|                  | AJ1             |      |      |       |           |             |
|                  | AK1             |      |      |       |           |             |

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## FOREIGN PATENT DOCUMENTS

| EXAMINER INITIAL | DOCUMENT NUMBER | DATE                 | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
|------------------|-----------------|----------------------|---------|-------|-----------|-------------|
|                  | AL1             | 0 109 567 A2         | 05/1984 | EP    |           | Yes<br>No   |
|                  | AM1             | 0 169 565 A2         | 01/1986 | EP    |           | Yes<br>No   |
|                  | AN1             | 0 199 173 A2<br>& A3 | 10/1986 | EP    |           | Yes<br>No   |
|                  | AO1             | 0 264 215 A2         | 04/1988 | EP    |           | Yes<br>No   |
|                  | AP1             | 0 272 198 A2<br>& A3 | 06/1988 | EP    |           | Yes<br>No   |

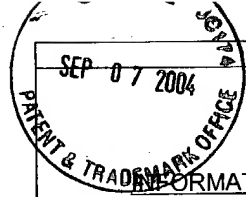
## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

|    |   |   |
|----|---|---|
| AR | 1 | Bandyopadhyay, A. and Zheng, Y.F., "Combining Both Micro-Code And Hardwired Control In RISC," 5 pages, (published in ACM SIGARCH Computer Architecture News, Vol. 15, Issue 4, pp. 11-15 (September 1987)). |
| AS | 1 | Bursky, D., "Software-Efficient RISC Core Trims System-Memory Needs," Reprinted from Electronic Design, Penton Publishing, Inc., 3 pages (March 20, 1995).  |
| AT | 1 | Hayashi, T. et al., "A 5.6-MIPS Call-Handling Processor for Switching Systems," IEEE Journal of Solid-State Circuits, Vol. 24, No. 4, IEEE, pp. 945-950 (August 1989).                                      |

EXAMINER

DATE CONSIDERED

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



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|---------------------|-----|--------------------|------|------|-------|-----------|-------------|
|                     | AA1 |                    |      |      |       |           |             |
|                     | AB2 |                    |      |      |       |           |             |
|                     | AC2 |                    |      |      |       |           |             |
|                     | AD2 |                    |      |      |       |           |             |
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|                     | AF2 |                    |      |      |       |           |             |
|                     | AG2 |                    |      |      |       |           |             |
|                     | AH2 |                    |      |      |       |           |             |
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## FOREIGN PATENT DOCUMENTS

| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER   | DATE    | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
|---------------------|-----|----------------------|---------|---------|-------|-----------|-------------|
|                     | AL2 | 0 324 308 A2<br>& A3 | 07/1989 | EP      |       |           | Yes<br>No   |
|                     | AM2 | 2 016 755 A          | 09/1979 | GB      |       |           | Yes<br>No   |
|                     | AN2 | 2 284 492 A          | 06/1995 | GB      |       |           | Yes<br>No   |
|                     | AO2 | 2 289 353 A          | 11/1995 | GB      |       |           | Yes<br>No   |
|                     | AP2 | 2 290 395 A          | 12/1995 | GB      |       |           | Yes<br>No   |

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

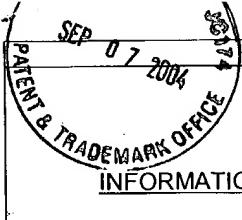
|  |    |   |   |
|--|----|---|---|
|  | AR | 2 | "High Performance Dual Architecture Processor," IBM Technical Disclosure Bulletin, Vol. 36, No. 2, IBM Corp., pp. 231-234 (February 1993).                                |
|  | AS | 2 | Intel486TM Microprocessor Family Programmer's Reference Manual, Intel Corporation, 1992, pp. 23-5, 23-6, and 23-9.  |
|  | AT | 2 | McNeley, K.J. and Milutinovic, V.M., "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," IEEE Micro, IEEE, pp. 60-72 (February 1987). |

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|---------------------|-----|--------------------|------|------|-------|-----------|-------------|
|                     | AA3 |                    |      |      |       |           |             |
|                     | AB3 |                    |      |      |       |           |             |
|                     | AC3 |                    |      |      |       |           |             |
|                     | AD3 |                    |      |      |       |           |             |
|                     | AE3 |                    |      |      |       |           |             |
|                     | AF3 |                    |      |      |       |           |             |
|                     | AG3 |                    |      |      |       |           |             |
|                     | AH3 |                    |      |      |       |           |             |
|                     | AI3 |                    |      |      |       |           |             |
|                     | AJ3 |                    |      |      |       |           |             |
|                     | AK3 |                    |      |      |       |           |             |

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| EXAMINER<br>INITIAL |     | DOCUMENT<br>NUMBER | DATE    | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |
|---------------------|-----|--------------------|---------|---------|-------|-----------|-------------|
|                     | AL3 | 96/24895           | 08/1996 | WO      |       |           | Yes<br>No   |
|                     | AM3 | 95/30187           | 11/1995 | WO      |       |           | Yes<br>No   |
|                     | AN3 | 95/30188           | 11/1995 | WO      |       |           | Yes<br>No   |
|                     | AO3 | 990394             | 06/1976 | CA      |       |           | Yes<br>No   |
|                     | AP3 |                    |         |         |       |           | Yes<br>No   |

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

|  |    |          |  |
|--|----|----------|--|
|  | AR | <u>3</u> | "LSI Tiny Risc development,"<br><a href="http://www.redhat.com/support/manuals/gnupro99r1/6_embed/emb09.html">http://www.redhat.com/support/manuals/gnupro99r1/6_embed/emb09.html</a> , 13 pages (April 2001). |
|  | AS | <u>3</u> | V <sub>R</sub> 4121 <sup>TM</sup> 64/32-Bit Microprocessor $\mu$ PD30121, NEC Corporation 1998, Document No. U13569EJ4V1UM00 (4 <sup>th</sup> edition), pages 1-19 and 103-131, Published July 2000.           |
|  | AT | <u>3</u> |  |

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